	,	Application No.	olication No. Applicant(s)		
	Notice of Allowability	09/680,665	DOR ET AL.	DOR ET AL.	
		Examiner	Art Unit		
		Chat C. Do	2124		
All claims being herewith (or pre NOTICE OF AL	the MAILING DATE of this communication appeallowable, PROSECUTION ON THE MERITS IS viously mailed), a Notice of Allowance (PTOL-85) LOWABILITY IS NOT A GRANT OF PATENT Rupon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in or other appropriate commits in the commits of the commits	n this application. If not includ unication will be mailed in due	led course. <b>THIS</b>	
1. X This com	munication is responsive to <u>09/20/04</u> .				
2. X The allow	ved claim(s) is/are <u>9-13 and 21-24</u> .				
3. ⊠ The draw	ings filed on <u>06 October 2000</u> are accepted by th	e Examiner.			
a)  All 1.   2.   3.   * Certified Applicant has noted below.	edgment is made of a claim for foreign priority une b) Some* c) None of the:  Certified copies of the priority documents have Certified copies of the priority documents have Copies of the certified copies of the priority do International Bureau (PCT Rule 17.2(a)). It copies not received:  THREE MONTHS FROM THE "MAILING DATE" Failure to timely comply will result in ABANDONM MONTH PERIOD IS NOT EXTENDABLE.	been received. been received in Application cuments have been receive	on No ed in this national stage applica		
	ITUTE OATH OR DECLARATION must be subm AL PATENT APPLICATION (PTO-152) which give			IOTICE OF	
(a) ☐ includ 1) [ (b) ☐ includ	TED DRAWINGS ( as "replacement sheets") must ding changes required by the Notice of Draftspers hereto or 2)  to Paper No./Mail Date ding changes required by the attached Examiner's	on's Patent Drawing Revie			
Identifying in each sheet. R	idicia such as the application number (see 37 CFR 1 eplacement sheet(s) should be labeled as such in t	84(c)) should be written on t he header according to 37 Cl	he drawings in the front (not the FR 1.121(d).	aback) of	
7. DEPOSI	T OF and/or INFORMATION about the depo examiner's comment regarding REQUIREMENT	sit of BIOLOGICAL MAT	ERIAL must be submitted.	Note the	
Attachment(s)	References Cited (PTO-892)	E □ Notice of Ir	Stormal Datont Application (DT)	O 152)	
<ol> <li>Notice of □</li> <li>Information</li> </ol>	Praftperson's Patent Drawing Review (PTO-948)  Disclosure Statements (PTO-1449 or PTO/SB/0	6. ⊠ Interview S Paper No.	nformal Patent Application (PTo Summary (PTO-413), /Mail Date <u>12/06/04</u> . S Amendment/Comment	J-162)	
	/Mail Date <u>09/20/04</u> Comment Regarding Requirement for Deposit Material	8. ⊠ Examiner's 9. □ Other	Statement of Reasons for Allo		
	•		PRIMARY EXAMIN	EH	

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## **EXAMINER'S STATEMENT OF REASONS FOR ALLOWANCE**

1. Claims 9-13 and 21-24 are allowed.

- 2. Claims 1-8 and 14-20 are cancelled.
- 3. The following is an examiner's statement of reasons for allowance:

The prior art of records fails to disclose or render obviousness an apparatus for performing a linear transformation on matrices including a control and address generation circuitry comprising: a comparator circuitry; a first set of multiplexers having at least one direct input for receiving transformation data and another input which the transformation data is fed via a corresponding inverter; a second set of multiplexers having at least one input connected to the output of the above multiplexers; and a last multiplier operating in combination with second set of multiplexers thereby providing the remaining portion of second address as cited in independent claim 9; an apparatus for performing a linear transformation on matrices comprising: a first and second inputs that receive input data and predetermined transformation data from means for reducing dimensions of and normalizing general real and complex matrices and control and address generation circuitry generates corresponding addresses for accessing cells of memory for controlling selection between a data receiving mode as cited in independent claim 21.

The closest found prior art is Nakai et al. (U.S. 6,115,728). Nakai et al. disclose an apparatus for performing a linear transformation on matrices data set including a control and address generation circuitry. However, Nakai et al. fail to disclose, teach, or suggest in detail the structure of a first, second, and last multiplexers to providing the

remaining portion of second address. In addition, Nakai et al. fail to disclose a means for reducing dimensions of and normalizing general real and complex matrices as seen above.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Chat C. Do whose telephone number is (571) 272-3721. The examiner can normally be reached on  $M \Rightarrow F$  from 7:00 AM to 5:30 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Chaki Kakali can be reached on (571) 272-3719. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Chat C. Do Examiner Art Unit 2124 Art Unit: 2124

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